



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

**Kinzer, et al.**

Serial No.: **10/613,326**

Filed: **July 3, 2003**

For: **Vertical Conduction Flip-Chip Device with  
Bump Contacts on Single Surface**

Art Unit: 2811

Examiner: Nadav. Ori

**INFORMATION DISCLOSURE STATEMENT**

Honorable Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir/Madam:

In this Information Disclosure Statement and pursuant to 37 C.F.R. §§ 1.56 and 1.97, Applicants hereby disclose to the Patent Office patents, publications or other information of which Applicants are aware. A list of references is included in Form 1449 submitted herewith.

The items identified in this Information Disclosure Statement may or may not be “material” as defined in 37 C.F.R. § 1.56, and the submission thereof by Applicants is not to be construed as an admission that any such patent, publication or other information referred to is material or considered to be material (37 C.F.R. § 1.97(h)), or even qualifies as “prior art” under 35 U.S.C. § 102 with respect to the present invention unless specifically designated by Applicants as such. Identification of any reference or patent herein is not an admission, nor is it to be construed as an admission, that it was invented prior to the invention disclosed herein.

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In accordance with 37 C.F.R. § 1.97 no fee is believed due at this time, since this Information Disclosure Statement is filed in accordance with 37 C.F.R. § 1.97(b)(4). This information disclosure statement is being filed before the mailing of a first Office action after the filing of a request for continued examination under §1.114. The Commissioner is hereby authorized to charge or credit Deposit Account Number 50-0731 for any deficiency or overpayment in the fees required for the filing of this Information Disclosure Statement.

Respectfully submitted,  
Farjami & Farjami LLP

Dated:

*MF*  
*10/15/10* *4/15/10*

By:

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